
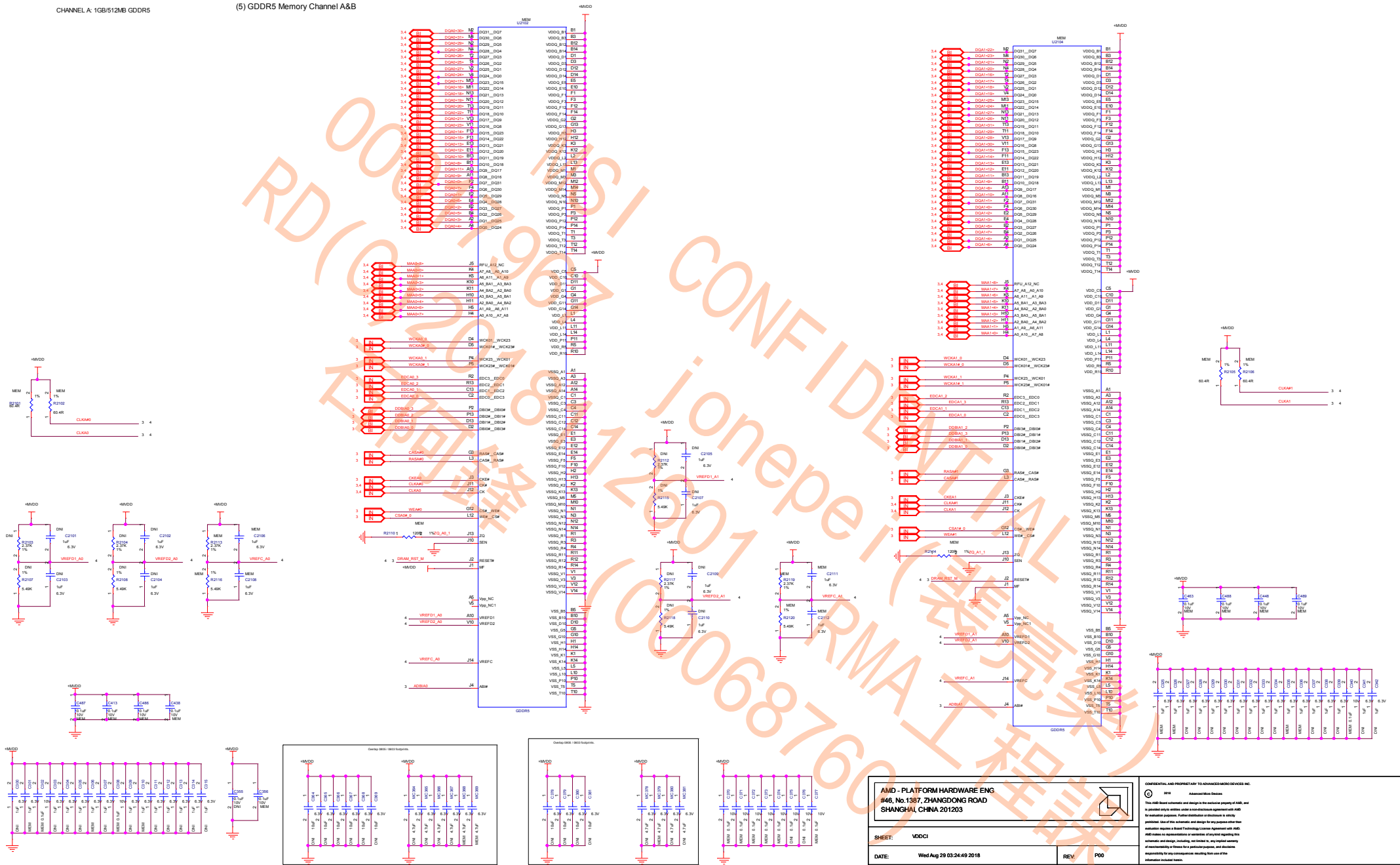


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SHEET: VDDCI					
DATE: Wed Aug 29 03:24:49 2018		REV: P00			
SHEET NUMBER: 3		OF 20		TITLE:	
DOCUMENT NUMBER: 105_012900_00A				XXX	
NOTES: NOTE					

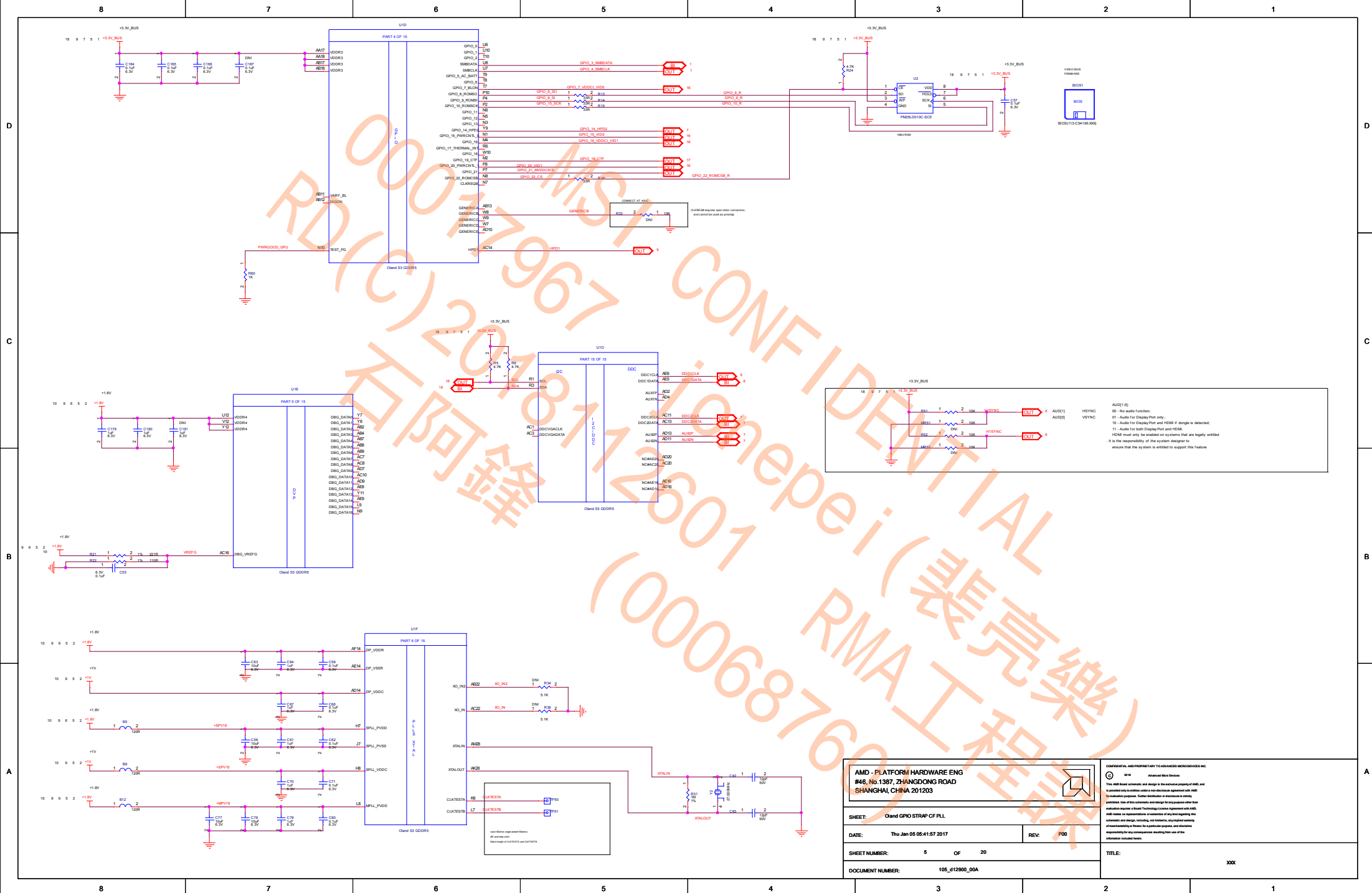


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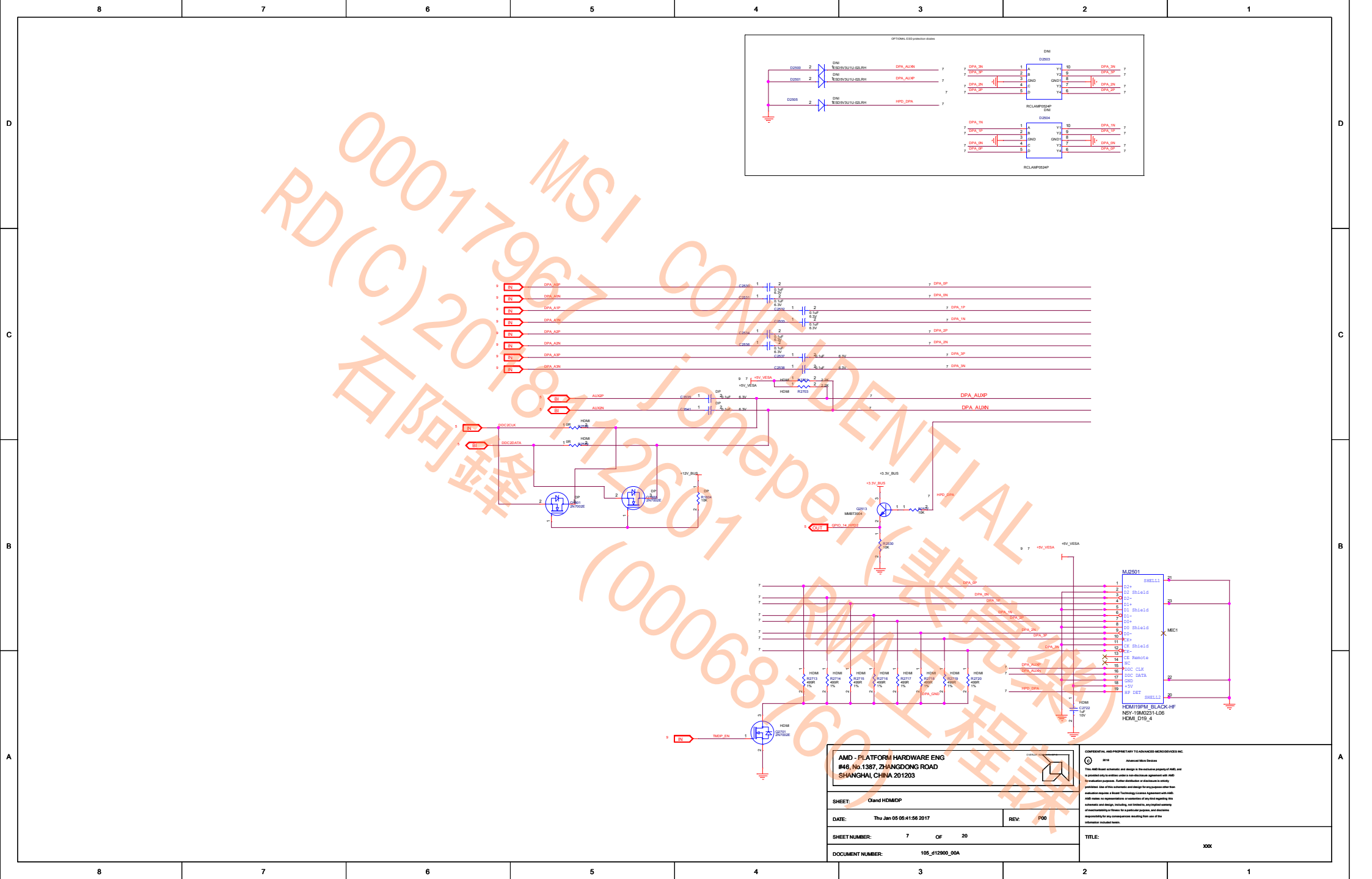
SHEET: VDDCI
DATE: Wed Aug 29 03:24:49 2018
SHEET NUMBER: 4 OF 20
DOCUMENT NUMBER: 105_012900_00A
NOTES: NOTE

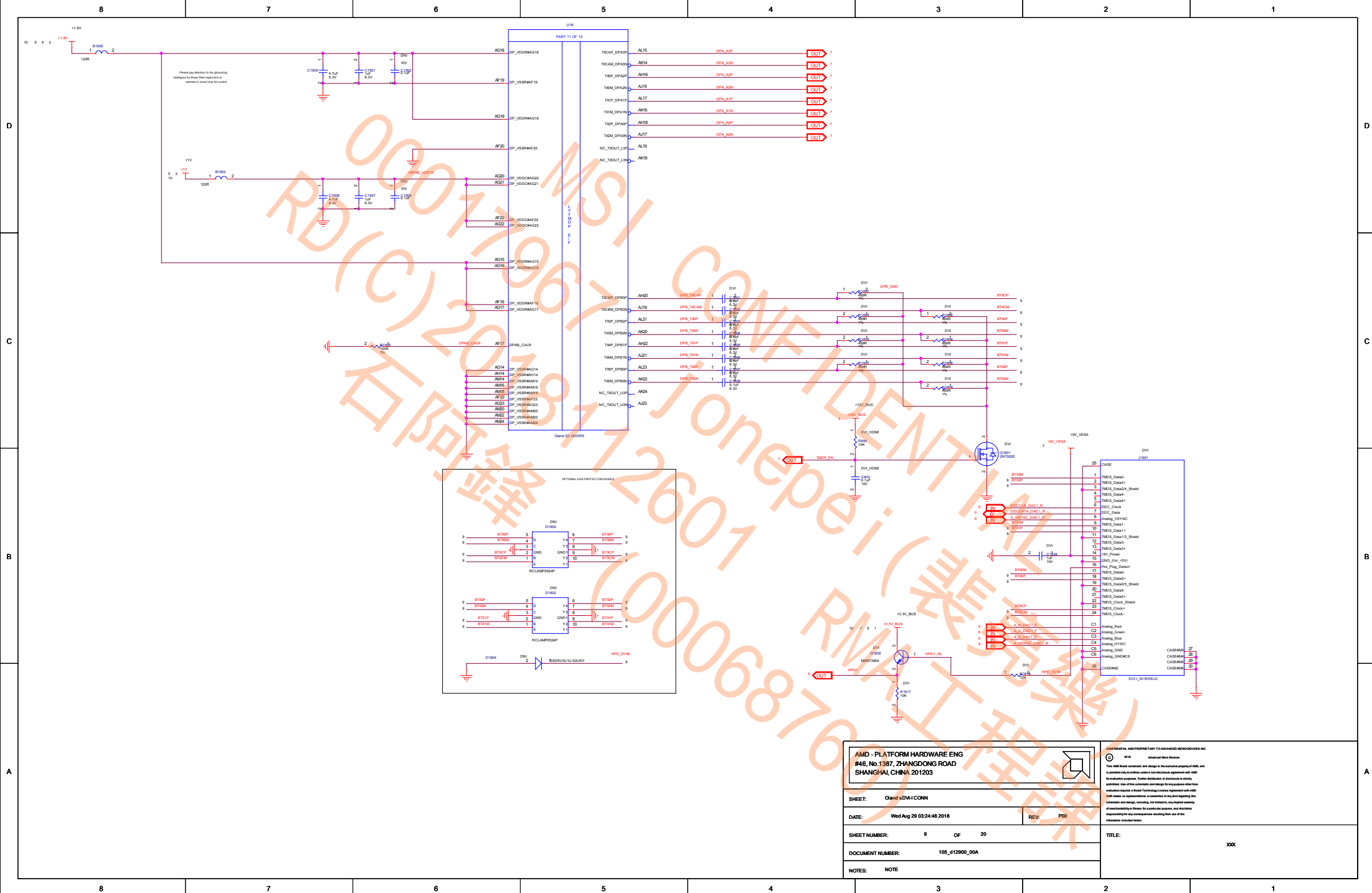
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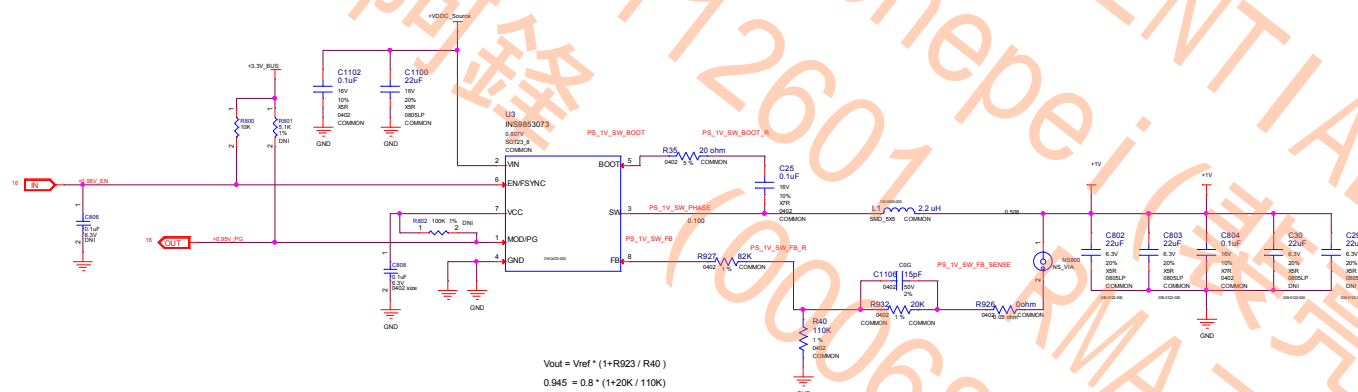


18ci203





LDO #1: Vin = +1.5V +/-2% Vout = +0.95V +/-2% Iout = 1.44A (TbV) RMS MAX
PCB: 50 to 70mm sq. copper area for cooling


$$V_{out} = V_{ref} * (1 + R_{923} / R_{40})$$
$$0.945 = 0.8 * (1 + 20K / 110K)$$
[illegible]

Regulator for M100

Use rVDDC_Sense to share L801 to eliminate PCIe 12V current slew rate over spec issue(0.1A/us)
rVDDC_Sense

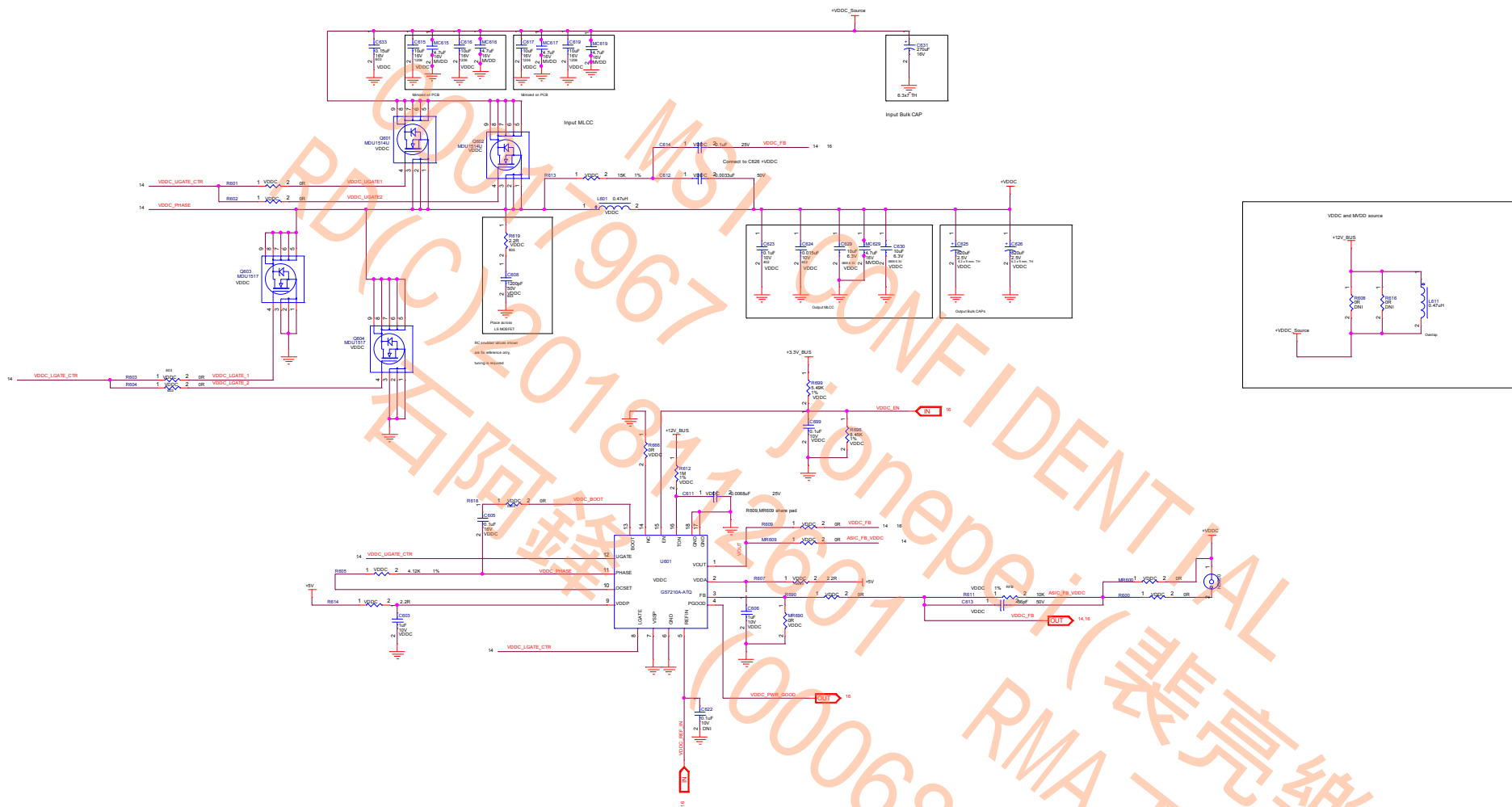
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DOCUMENT NUMBER: 105_d12900_00A	
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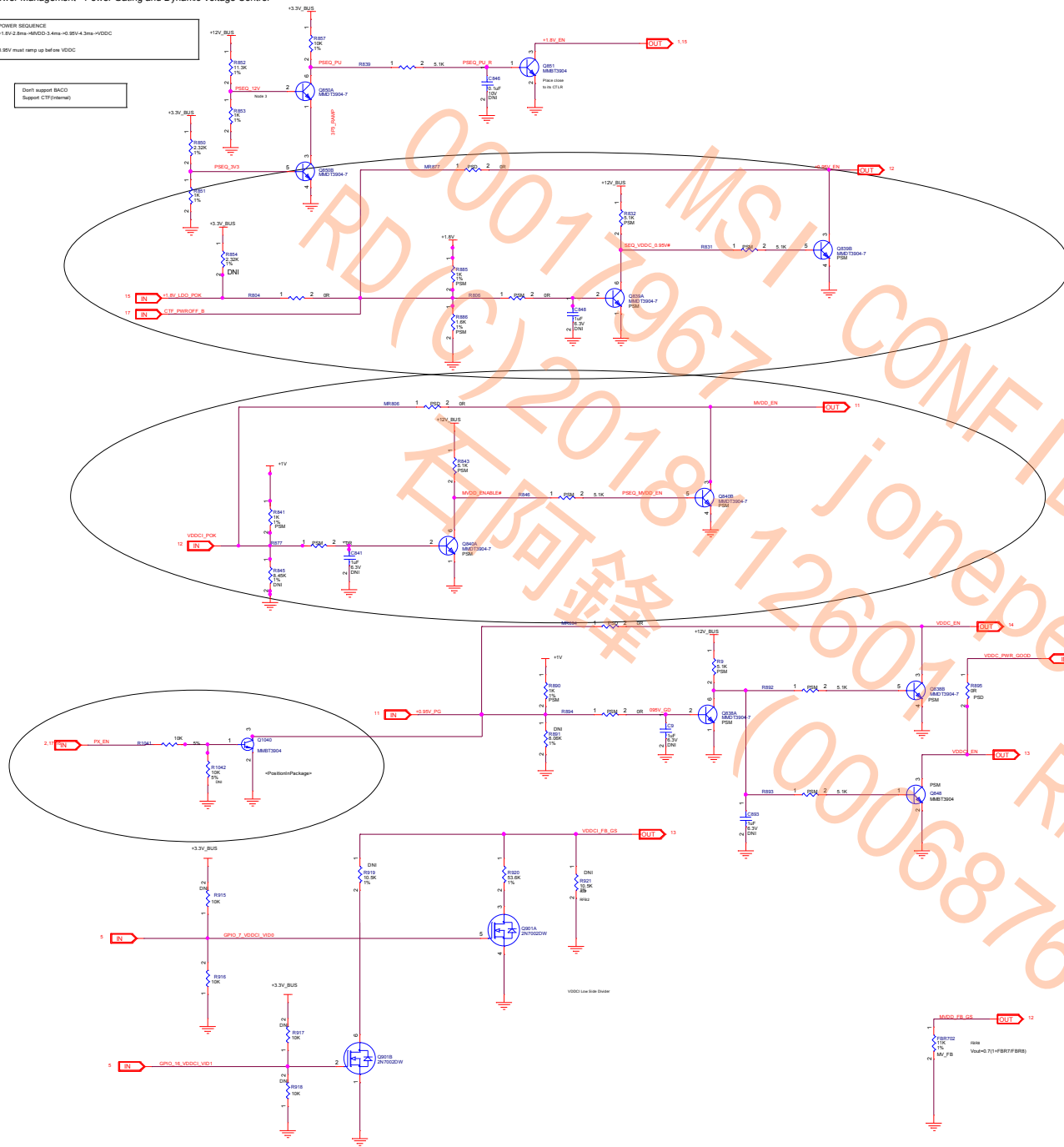
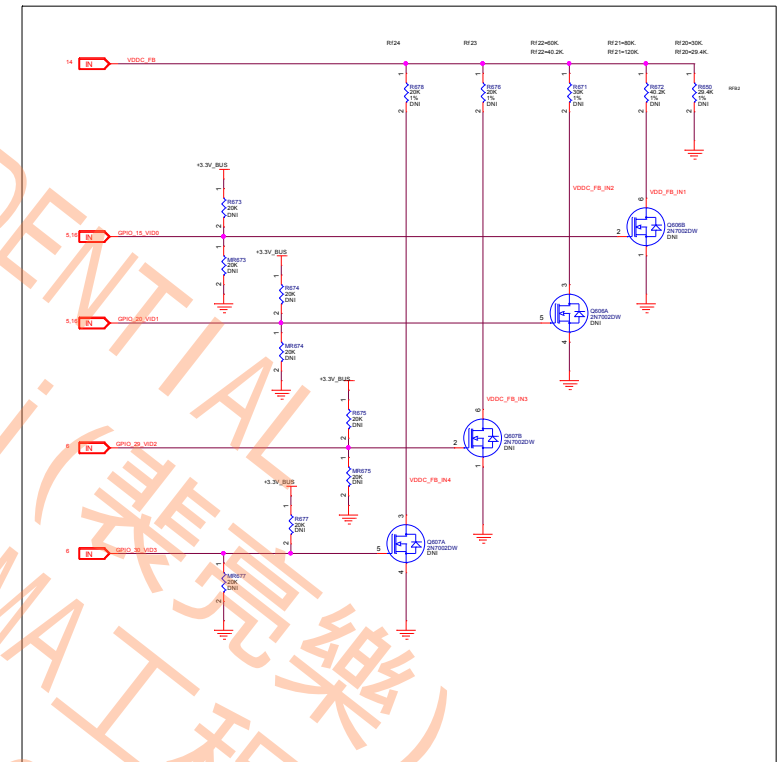


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DOCUMENT NUMBER: 105_d12900_00A

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Don't support BACO
Support CTF (Internal)

[illegible]

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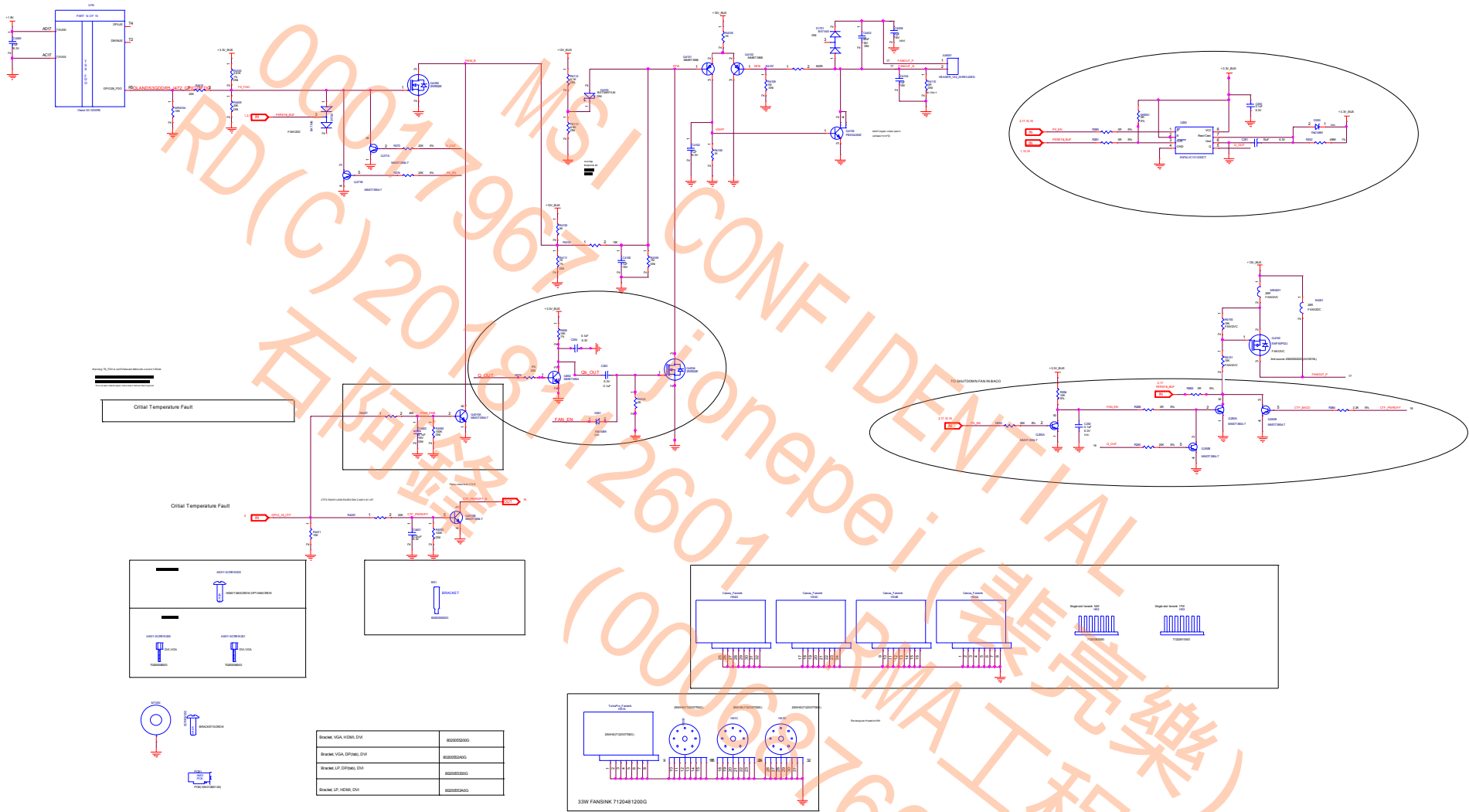
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XXX

Initial PMSI	OPNDA-0.0 Pin-Header OPNDA-0.0 Pin-Header
Initial PMSI (0.0) PMSI	OPNDA-0.0 Pin-Header OPNDA-0.0 Pin-Header

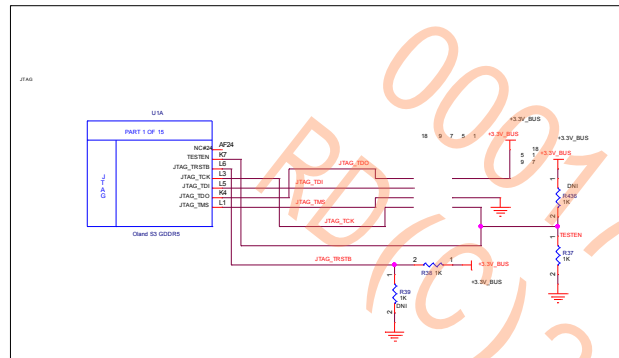


Mechanical and Thermal Management



Shimadzu VCA 1000L D14	800000000
Shimadzu VCA 1000L D14	800000000
Shimadzu VCA 1000L D14	800000000
Shimadzu VCA 1000L D14	800000000

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SHEET	VIDEO	REV	REV
DATE	2014-05-20 (0.0)	REV	REV
SHEET NUMBER	17	REV	REV
DOCUMENT NUMBER	XXXXXXXXXX		
NOTE	NOTE		

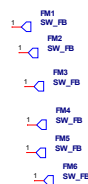


TOP
Single end
Memory data
45 ohm +/- 5 ohm
5.12 mils

Bottom
Single end
Address trunk
45 ohm +/- 5 ohm
5.12 mils


TOP
Different
TMDS
85 ohm +/- 10 %
4.33 mils / 5.51 mils

Bottom
Different
PEX PCIE
85 ohm +/- 10 %
4.33 mils / 5.51 mils



	8	7	6	5	4	3	2	1	
D			TITLE		SCHEMATIC NO.		DATE		
			Client ID: Q20RS 1GB HBM2P+4DVI-IVGA		105-012900-00A				
	REVISION HISTORY		NOTE: THIS DOCUMENT REPRESENTS THE PCB. IT DOES NOT REPRESENT ANY SPECIFIC DUTY. FOR CORRELATED OFFICE (CONFIDENTIAL) VALUES, SEE POWER CORRELATION (THE PRESENT) SPECIFICATION. PLEASE CONSULT AND REPRESENTATIVE TO CORRELATION AND CORRELATION TO THE APPLICATION (BRAIN).					REV	
	SCH REV	PCB REV	DATE	REVISION DESCRIPTION					POB
	01	001	2017-01-01	Initial Board/PCB schematic based on Q20RS 1GB					
C									
B									
A									

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